

# Pin List: FOMD-ACV-A4

2020-09-30

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Top	1		VDC_IN	VDC_IN							
SODIMM Bottom	2		VDC_IN	VDC_IN							
SODIMM Top	3		VDC_IN	VDC_IN							
SODIMM Bottom	4		VDC_IN	VDC_IN							
SODIMM Top	5		VDC_IN	VDC_IN							
SODIMM Bottom	6		PWRGOOD	PWRGOOD				VCCPGM		J5-1	
SODIMM Top	7		DGND	DGND							
SODIMM Bottom	8		DGND	DGND							
SODIMM Top	9		+2.5VDC	+2.5VDC							
SODIMM Bottom	10		+2.5VDC	+2.5VDC							
SODIMM Top	11	N1	L13N	NC	37.400	TX	2A	VCCIO1	50 Ohm Single-Ended	J2-4	J10-10
SODIMM Bottom	12	C2	L9P	NC	44.677	TX	2A	VCCIO1	50 Ohm Single-Ended	J5-4	
SODIMM Top	13	N2	L13P	NC	38.028	TX	2A	VCCIO1	50 Ohm Single-Ended	J2-3	J10-4
SODIMM Bottom	14	C1	L9N	NC	41.756	TX	2A	VCCIO1	50 Ohm Single-Ended	J5-3	
SODIMM Top	15	U1	L14N	NC	30.926	RX	2A	VCCIO1	50 Ohm Single-Ended	J2-6	J10-9
SODIMM Bottom	16	D3	L11P	NC	43.128	RX	2A	VCCIO1	50 Ohm Single-Ended	J5-6	
SODIMM Top	17	U2	L14P	NC	31.298	RX	2A	VCCIO1	50 Ohm Single-Ended	J2-5	J10-3
SODIMM Bottom	18	E2	L11N	NC	41.205	RX	2A	VCCIO1	50 Ohm Single-Ended	J5-5	
SODIMM Top	19	W2	L15N	NC	29.921	RX	2A	VCCIO1	50 Ohm Single-Ended	J2-8	J10-8
SODIMM Bottom	20	G2	L10P	NC	39.480	RX	2A	VCCIO1	50 Ohm Single-Ended	J5-8	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Top	21	Y3	L15P	NC	29.834	RX	2A	VCCIO1	50 Ohm Single-Ended	J2-7	J10-2
SODIMM Bottom	22	G1	L10N	NC	37.249	RX	2A	VCCIO1	50 Ohm Single-Ended	J5-7	
SODIMM Top	23	AA2	L16P	NC	27.775	TX	2A	VCCIO1	50 Ohm Single-Ended	J2-10	J10-7
SODIMM Bottom	24	L2	L12P	NC	33.280	TX	2A	VCCIO1	50 Ohm Single-Ended	J5-10	
SODIMM Top	25	AA1	L16N	NC	25.133	TX	2A	VCCIO1	50 Ohm Single-Ended	J2-9	J10-1
SODIMM Bottom	26	L1	L12N	NC	31.274	TX	2A	VCCIO1	50 Ohm Single-Ended	J5-9	
SODIMM Top	27		DGND	DGND							
SODIMM Bottom	28		DGND	DGND							
SODIMM Top	29	L6	MSEL0	MSEL0			9A	VCCPGM	50 Ohm Single-Ended	J2-11	
SODIMM Bottom	30	K6	CONF_DONE	CONF_DONE			9A	VCCPGM	50 Ohm Single-Ended	J5-12	
SODIMM Top	31	J6	MSEL1	MSEL1			9A	VCCPGM	50 Ohm Single-Ended	J2-14	
SODIMM Bottom	32	G5	NCE	NCE			9A	VCCPGM	50 Ohm Single-Ended	J5-11	
SODIMM Top	33	A2	MSEL2	MSEL2			9A	VCCPGM	50 Ohm Single-Ended	J2-13	
SODIMM Bottom	34	A4	NCONFIG	NCONFIG			9A	VCCPGM	50 Ohm Single-Ended	J5-14	
SODIMM Top	35	E5	MSEL3	MSEL3			9A	VCCPGM	50 Ohm Single-Ended	J2-16	
SODIMM Bottom	36	H5	NSTATUS	NSTATUS			9A	VCCPGM	50 Ohm Single-Ended	J5-13	
SODIMM Top	37	F3	MSEL4	MSEL4			9A	VCCPGM	50 Ohm Single-Ended	J2-15	
SODIMM Bottom	38	V3	DCLK	DCLK			3A	VCCPGM	50 Ohm Single-Ended	J5-16	
SODIMM Top	39		VCCBAT	VCCBAT						J2-17	
SODIMM Bottom	40	AB4	DATA0	DATA0			3A	VCCIO1	50 Ohm Single-Ended	J5-15	
SODIMM Top	41	M5	JTAG_TDO	JTAG_TDO			3A	VCCIO1	50 Ohm Single-Ended	J2-20	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Bottom	42	AB3	DATA1	DATA1			3A	VCCIO1	50 Ohm Single-Ended	J5-18	
SODIMM Top	43	P5	JTAG_TMS	JTAG_TMS			3A	VCCIO1	50 Ohm Single-Ended	J2-19	
SODIMM Bottom	44	AA5	DATA2	DATA2			3A	VCCIO1	50 Ohm Single-Ended	J5-17	
SODIMM Top	45	V5	JTAG_TCK	JTAG_TCK			3A	VCCIO1	50 Ohm Single-Ended	J3-2	
SODIMM Bottom	46		DGND	DGND							
SODIMM Top	47	W5	JTAG_TDI	JTAG_TDI			3A	VCCIO1	50 Ohm Single-Ended	J3-1	
SODIMM Bottom	48	T4	DATA3	DATA3			3A	VCCIO1	50 Ohm Single-Ended	J5-20	
SODIMM Top	49		VCCPGM	VCCPGM						J3-3	
SODIMM Bottom	50	R4	NCSO_DATA4	NCSO_DATA4			9A	VCCPGM	50 Ohm Single-Ended	J5-19, J6-3	
SODIMM Top	51	M6	B7N	B7N	29.332	RX	3A	VCCPGM	50 Ohm Single-Ended	J3-6	J11-10
SODIMM Bottom	52	N6	B3P	B3P	30.198	RX	3A	VCCPGM	50 Ohm Single-Ended	J6-2	
SODIMM Top	53	M7	B7P	B7P	29.803	RX	3A	VCCPGM	50 Ohm Single-Ended	J3-5	J11-4
SODIMM Bottom	54	P6	B3N	B3N	29.291	RX	3A	VCCPGM	50 Ohm Single-Ended	J6-1	
SODIMM Top	55	R5	B1P	B1P	25.448	RX	3A	VCCPGM	50 Ohm Single-Ended	J3-8	J11-9
SODIMM Bottom	56	P7	B8P	B8P	28.808	TX	3A	VCCPGM	50 Ohm Single-Ended	J6-6	
SODIMM Top	57	R6	B1N	B1N	25.388	RX	3A	VCCPGM	50 Ohm Single-Ended	J3-7	J11-3
SODIMM Bottom	58	R7	B8N	B8N	28.238	TX	3A	VCCPGM	50 Ohm Single-Ended	J6-5	
SODIMM Top	59	T7	B5N	B5N	25.395	RX	3A	VCCPGM	50 Ohm Single-Ended	J3-10	J11-8
SODIMM Bottom	60		DGND	DGND							
SODIMM Top	61	T8	B5P	B5P	24.966	RX	3A	VCCIO1	50 Ohm Single-Ended	J3-9	J11-2
SODIMM Bottom	62	U6	B6N	B6N	22.956	TX	3A	VCCIO1	50 Ohm Single-Ended	J6-8	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Top	63		DGND	DGND							
SODIMM Bottom	64	V6	B6P	B6P	21.308	TX	3A	VCCIO1	50 Ohm Single-Ended	J6-7	
SODIMM Top	65	W8	B4N	B4N	22.019	TX	3A	VCCIO1	50 Ohm Single-Ended	J3-12	J11-7
SODIMM Bottom	66	U7	B2N	B2N	24.370	TX	3A	VCCIO1	50 Ohm Single-Ended	J6-10	
SODIMM Top	67	W9	B4P	B4P	22.802	TX	3A	VCCIO1	50 Ohm Single-Ended	J3-11	J11-1
SODIMM Bottom	68	U8	B2P	B2P	24.084	TX	3A	VCCIO1	50 Ohm Single-Ended	J6-9	
SODIMM Top	69	P8	B11N	B11N	26.647	RX	3B	VCCIO2	50 Ohm Single-Ended	J3-14	
SODIMM Bottom	70	T9	B14N	B14N	25.253	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-12	
SODIMM Top	71	N8	B11P	B11P	27.610	RX	3B	VCCIO2	50 Ohm Single-Ended	J3-13	
SODIMM Bottom	72	U10	B14P	B14P	24.775	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-11	
SODIMM Top	73	AB5	B9P	B9P	12.198	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-16	
SODIMM Bottom	74		DGND	DGND							
SODIMM Top	75	AB6	B9N	B9N	12.557	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-15	
SODIMM Bottom	76	V9	B10N	B10N	19.889	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-14	
SODIMM Top	77		DGND	DGND							
SODIMM Bottom	78	V10	B10P	B10P	19.652	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-13	
SODIMM Top	79	AA7	B12N	B12N	13.600	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-18	
SODIMM Bottom	80	P9	B23N	B23N	23.036	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-16	
SODIMM Top	81	AB7	B12P	B12P	12.471	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-17	
SODIMM Bottom	82	N9	B23P	B23P	24.210	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-15	
SODIMM Top	83	AA8	B13N	B13N	13.696	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-20	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Bottom	84	R10	B22N	B22N	22.984	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-18	
SODIMM Top	85	AB8	B13P	B13P	12.820	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-19	
SODIMM Bottom	86	R11	B22P	B22P	22.847	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-17	
SODIMM Top	87	Y9	B17P	B17P	14.894	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-22	J13-10
SODIMM Bottom	88		DGND	DGND							
SODIMM Top	89	Y10	B17N	B17N	15.600	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-21	J13-4
SODIMM Bottom	90	R9	B18P	B18P	19.975	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-20	
SODIMM Top	91		VREF1	VREF1						J3-23, J6-23	
SODIMM Bottom	92	T10	B18N	B18N	18.252	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-19	
SODIMM Top	93	AA9	B16P	B16P	11.984	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-26	J13-9
SODIMM Bottom	94	M8	B15N_CLK0N	B15N_CLK0N	24.089	RX	3B	VCCIO2	100 Ohm Differential	J6-22	
SODIMM Top	95	AA10	B16N	B16N	11.709	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-25	J13-3
SODIMM Bottom	96	M9	B15P_CLK0P	B15P_CLK0P	24.089	RX	3B	VCCIO2	100 Ohm Differential	J6-21	
SODIMM Top	97	AB10	B21N_BL_CLKOUT N	B21N_BL_CLKOUT N	10.553	TX	3B	VCCIO2	100 Ohm Differential	J3-28	J13-8
SODIMM Bottom	98	P12	B20P	B20P	20.223	TX	3B	VCCIO2	50 Ohm Single-Ended	J6-26	
SODIMM Top	99	AB11	B21P_BL_CLKOUT P	B21P_BL_CLKOUT P	10.553	TX	3B	VCCIO2	100 Ohm Differential	J3-27	J13-2
SODIMM Bottom	100	R12	B20N	B20N	18.982	TX	3B	VCCIO2	50 Ohm Single-Ended	J6-25	
SODIMM Top	101	Y11	B24N	B24N	13.544	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-30	J13-7
SODIMM Bottom	102		DGND	DGND							
SODIMM Top	103	AA12	B24P	B24P	11.871	TX	3B	VCCIO2	50 Ohm Single-Ended	J3-29	J13-1
SODIMM Bottom	104	U11	B19N	B19N	15.595	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-28	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Top	105		VCCIO1	VCCIO1						J3-31, J6-39	
SODIMM Bottom	106	U12	B19P	B19P	15.671	RX	3B	VCCIO2	50 Ohm Single-Ended	J6-27	
SODIMM Top	107	AB12	B25P	B25P	9.793	TX	4A	VCCIO2	50 Ohm Single-Ended	J3-34	
SODIMM Bottom	108	U13	B26P	B26P	15.836	RX	4A	VCCIO2	50 Ohm Single-Ended	J6-30	
SODIMM Top	109	AB13	B25N	B25N	9.937	TX	4A	VCCIO2	50 Ohm Single-Ended	J3-33	
SODIMM Bottom	110	V13	B26N	B26N	14.588	RX	4A	VCCIO2	50 Ohm Single-Ended	J6-29	
SODIMM Top	111	AA13	B28P	B28P	10.938	TX	4A	VCCIO2	50 Ohm Single-Ended	J3-36	
SODIMM Bottom	112	T12	B27N	B27N	16.250	RX	4A	VCCIO2	50 Ohm Single-Ended	J6-34	
SODIMM Top	113	AA14	B28N	B28N	10.734	TX	4A	VCCIO2	50 Ohm Single-Ended	J3-35	
SODIMM Bottom	114	T13	B27P	B27P	16.091	RX	4A	VCCIO2	50 Ohm Single-Ended	J6-33	
SODIMM Top	115	Y14	B30N	B30N	12.014	RX	4A	VCCIO2	50 Ohm Single-Ended	J3-38	
SODIMM Bottom	116		DGND	DGND							
SODIMM Top	117	Y15	B30P	B30P	11.377	RX	4A	VCCIO2	50 Ohm Single-Ended	J3-37	
SODIMM Bottom	118	T14	B35N	B35N	16.576	RX	4A	VCCIO2	50 Ohm Single-Ended	J6-36	
SODIMM Top	119		VCCPD1	VCCPD1						J3-39, J6-31	
SODIMM Bottom	120	U15	B35P	B35P	16.003	RX	4A	VCCIO2	50 Ohm Single-Ended	J6-35	
SODIMM Top	121	AB15	B29N	B29N	8.649	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-2	
SODIMM Bottom	122	V14	B31N_CLK2N	B31N_CLK2N	12.741	RX	4A	VCCIO2	100 Ohm Differential	J6-38	
SODIMM Top	123	AA15	B29P	B29P	9.811	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-1	
SODIMM Bottom	124	V15	B31P_CLK2P	B31P_CLK2P	12.741	RX	4A	VCCIO2	100 Ohm Differential	J6-37	
SODIMM Top	125	R14	B43P	B43P	15.834	RX	4A	VCCIO2	50 Ohm Single-Ended	J4-4	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Bottom	126	V16	B39N_CLK3N	B39N_CLK3N	13.239	RX	4A	VCCIO2	100 Ohm Differential	J7-2	
SODIMM Top	127	P14	B43N	B43N	16.766	RX	4A	VCCIO2	50 Ohm Single-Ended	J4-3	
SODIMM Bottom	128	W16	B39P_CLK3P	B39P_CLK3P	12.989	RX	4A	VCCIO2	100 Ohm Differential	J7-1	
SODIMM Top	129	Y16	B34N	B34N	10.492	RX	4A	VCCIO2	50 Ohm Single-Ended	J4-6	
SODIMM Bottom	130		DGND	DGND							
SODIMM Top	131	Y17	B34P	B34P	10.235	RX	4A	VCCIO2	50 Ohm Single-Ended	J4-5	
SODIMM Bottom	132	U16	B47N	B47N	14.714	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-4	
SODIMM Top	133		DGND	DGND							
SODIMM Bottom	134	U17	B47P	B47P	14.525	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-3	
SODIMM Top	135	AB17	B32N	B32N	7.738	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-8	
SODIMM Bottom	136	AA17	B36N	B36N	10.216	TX	4A	VCCIO2	50 Ohm Single-Ended	J7-6	
SODIMM Top	137	AB18	B32P	B32P	7.903	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-7	
SODIMM Bottom	138	AA18	B36P	B36P	10.179	TX	4A	VCCIO2	50 Ohm Single-Ended	J7-5	
SODIMM Top	139	AA19	B37N	B37N	9.396	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-10	
SODIMM Bottom	140	V18	B46P	B46P	13.262	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-8	
SODIMM Top	141	AA20	B37P	B37P	9.235	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-9	
SODIMM Bottom	142	V19	B46N	B46N	13.097	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-7	
SODIMM Top	143	AB20	B33N	B33N	8.087	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-12	J12-10
SODIMM Bottom	144		DGND	DGND							
SODIMM Top	145	AB21	B33P	B33P	8.152	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-11	J12-4
SODIMM Bottom	146	Y19	B42P	B42P	10.831	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-10	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Top	147		DGND	DGND							
SODIMM Bottom	148	Y20	B42N	B42N	10.908	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-9	
SODIMM Top	149	AB22	B40N	B40N	8.069	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-14	J12-9
SODIMM Bottom	150	W19	B38P	B38P	12.622	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-12	
SODIMM Top	151	AA22	B40P	B40P	9.240	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-13	J12-3
SODIMM Bottom	152	V20	B38N	B38N	13.518	RX	4A	VCCIO2	50 Ohm Single-Ended	J7-11	
SODIMM Top	153	Y22	B41N	B41N	10.212	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-16	J12-8
SODIMM Bottom	154	Y21	B44N	B44N	11.169	TX	4A	VCCIO2	50 Ohm Single-Ended	J7-14	
SODIMM Top	155	W22	B41P	B41P	11.480	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-15	J12-2
SODIMM Bottom	156	W21	B44P	B44P	12.219	TX	4A	VCCIO2	50 Ohm Single-Ended	J7-13	
SODIMM Top	157	U20	B48P	B48P	17.680	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-18	J12-7
SODIMM Bottom	158		DGND	DGND							
SODIMM Top	159	U21	B48N	B48N	16.527	TX	4A	VCCIO2	50 Ohm Single-Ended	J4-17	J12-1
SODIMM Bottom	160	V21	B45P	B45P	13.691	TX	4A	VCCIO2	50 Ohm Single-Ended	J7-16	
SODIMM Top	161		VREF2	VREF2						J4-19, J7-19	
SODIMM Bottom	162	U22	B45N	B45N	14.306	TX	4A	VCCIO2	50 Ohm Single-Ended	J7-15	
SODIMM Top	163	R15	R4N	R4N	23.786	RX	5A	VCCIO1	50 Ohm Single-Ended	J4-22	
SODIMM Bottom	164	T17	R2N	R2N	19.843	RX	5A	VCCIO1	50 Ohm Single-Ended	J7-18	
SODIMM Top	165	T15	R4P	R4P	23.543	RX	5A	VCCIO1	50 Ohm Single-Ended	J4-21	
SODIMM Bottom	166	T18	R2P	R2P	19.382	RX	5A	VCCIO1	50 Ohm Single-Ended	J7-17	
SODIMM Top	167	T22	R3P	R3P	16.867	TX	5A	VCCIO1	50 Ohm Single-Ended	J4-24	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Bottom	168	T19	R1P	R1P	18.921	TX	5A	VCCIO1	50 Ohm Single-Ended	J7-22	
SODIMM Top	169	R22	R3N	R3N	17.708	TX	5A	VCCIO1	50 Ohm Single-Ended	J4-23	
SODIMM Bottom	170	T20	R1N	R1N	18.489	TX	5A	VCCIO1	50 Ohm Single-Ended	J7-21	
SODIMM Top	171	P17	R8N	R8N	24.224	RX	5A	VCCIO1	50 Ohm Single-Ended	J4-26	
SODIMM Bottom	172		DGND	DGND							
SODIMM Top	173	P16	R8P	R8P	25.698	RX	5A	VCCIO1	50 Ohm Single-Ended	J4-25	
SODIMM Bottom	174	R17	R6N	R6N	23.586	RX	5A	VCCIO1	50 Ohm Single-Ended	J7-24	
SODIMM Top	175		VCCIO2	VCCIO2						J4-27, J7-27	
SODIMM Bottom	176	R16	R6P	R6P	25.449	RX	5A	VCCIO1	50 Ohm Single-Ended	J7-23	
SODIMM Top	177	R21	R5P	R5P	20.565	TX	5A	VCCIO1	50 Ohm Single-Ended	J4-30	
SODIMM Bottom	178	P18	R7N	R7N	23.762	TX	5A	VCCIO1	50 Ohm Single-Ended	J7-26	
SODIMM Top	179	P22	R5N	R5N	20.188	TX	5A	VCCIO1	50 Ohm Single-Ended	J4-29	
SODIMM Bottom	180	P19	R7P	R7P	23.603	TX	5A	VCCIO1	50 Ohm Single-Ended	J7-25	
SODIMM Top	181	N21	R10N	R10N	22.448	TX	5B	VCCIO1	50 Ohm Single-Ended	J4-32	
SODIMM Bottom	182	N16	R9P_CLK6P	R9P_CLK6P	27.113	RX	5B	VCCIO1	100 Ohm Differential	J7-30	
SODIMM Top	183	N20	R10P	R10P	24.374	TX	5B	VCCIO1	50 Ohm Single-Ended	J4-31	
SODIMM Bottom	184	M16	R9N_CLK6N	R9N_CLK6N	27.283	RX	5B	VCCIO1	100 Ohm Differential	J7-29	
SODIMM Top	185	M21	R14N	R14N	23.989	TX	5B	VCCIO1	50 Ohm Single-Ended	J4-34	
SODIMM Bottom	186		DGND	DGND							
SODIMM Top	187	M20	R14P	R14P	26.267	TX	5B	VCCIO1	50 Ohm Single-Ended	J4-33	
SODIMM Bottom	188	N19	R11P	R11P	26.641	RX	5B	VCCIO1	50 Ohm Single-Ended	J7-32	

Connector	Pin	FPGA Pin	Description (A4)	Description (A5+A7)	Length (mm)	FPGA LVDS Direction	I/O Bank	I/O Voltage Domain	Controlled Impedance	BRKD-ACV 2mm	BRKD-ACV POD
SODIMM Top	189		VCCPD2	VCCPD2						J4-35, J7-37	
SODIMM Bottom	190	M18	R11N	R11N	28.492	RX	5B	VCCIO1	50 Ohm Single-Ended	J7-31	
SODIMM Top	191	M22	R12P_BR_CLKOUT_P	R12P_BR_CLKOUT_P	24.150	TX	5B	VCCIO1	100 Ohm Differential	J4-38	
SODIMM Bottom	192	L17	R13N	R13N	30.577	RX	5B	VCCIO1	50 Ohm Single-Ended	J7-34	
SODIMM Top	193	L22	R12N_BR_CLKOUT_N	R12N_BR_CLKOUT_N	24.260	TX	5B	VCCIO1	100 Ohm Differential	J4-37	
SODIMM Bottom	194	K17	R13P	R13P	32.045	RX	5B	VCCIO1	50 Ohm Single-Ended	J7-33	
SODIMM Top	195	K22	R16N	R16N	26.526	TX	5B	VCCIO1	50 Ohm Single-Ended	J4-40	
SODIMM Bottom	196	L18	R15N	R15N	30.967	RX	5B	VCCIO1	50 Ohm Single-Ended	J7-36	
SODIMM Top	197	K21	R16P	R16P	28.506	TX	5B	VCCIO1	50 Ohm Single-Ended	J4-39	
SODIMM Bottom	198	L19	R15P	R15P	30.706	RX	5B	VCCIO1	50 Ohm Single-Ended	J7-35	
SODIMM Top	199		DGND	DGND							
SODIMM Bottom	200		DGND	DGND							
SODIMM Top	201		EEPROM_SDA	EEPROM_SDA				EEPROM_VCC		J7-39	
SODIMM Bottom	202		EEPROM_SCL	EEPROM_SCL				EEPROM_VCC		J7-40	
SODIMM Top	203		DGND	DGND							
SODIMM Bottom	204		EEPROM_VCC	EEPROM_VCC						J7-38	