

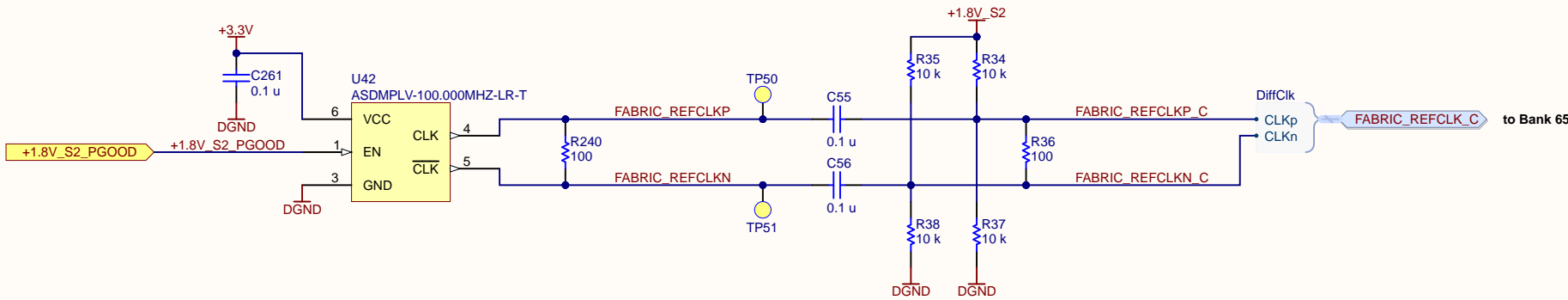
A

B

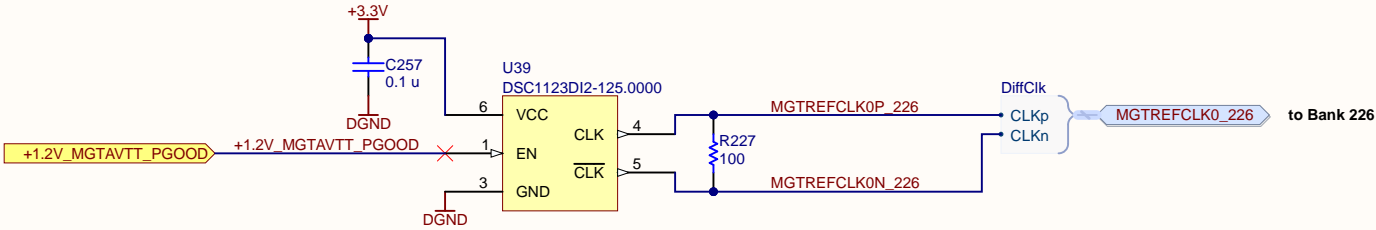
C

D

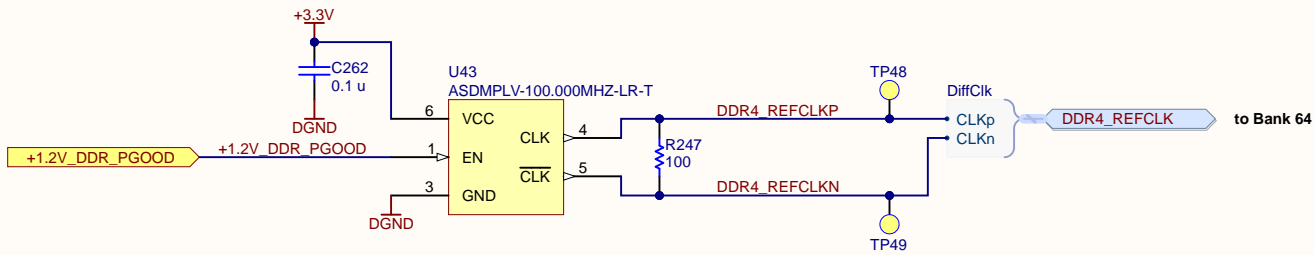
Fabric Reference Clock



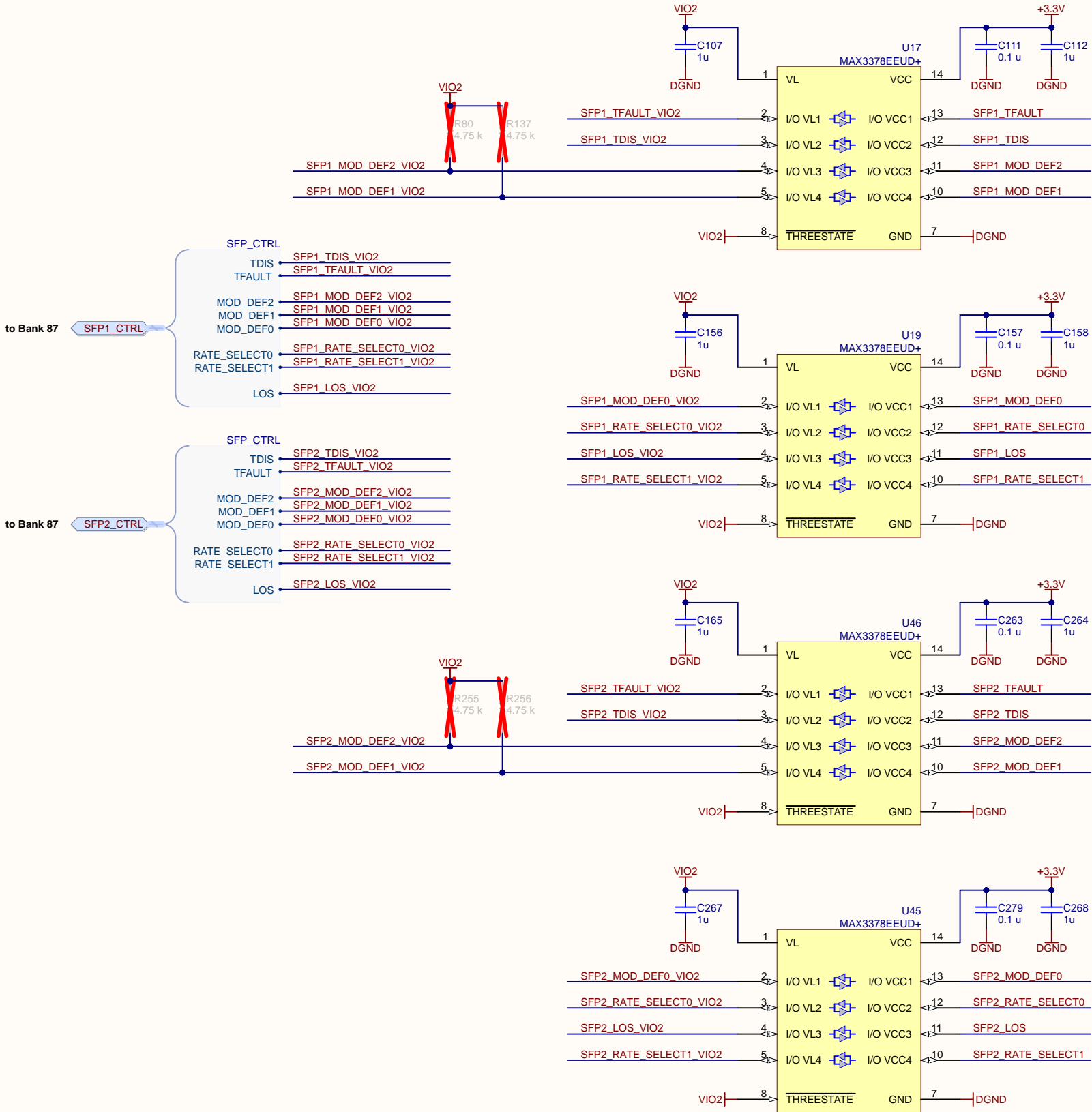
MGT Bank 226 Reference Clock
(SFP, SMA)



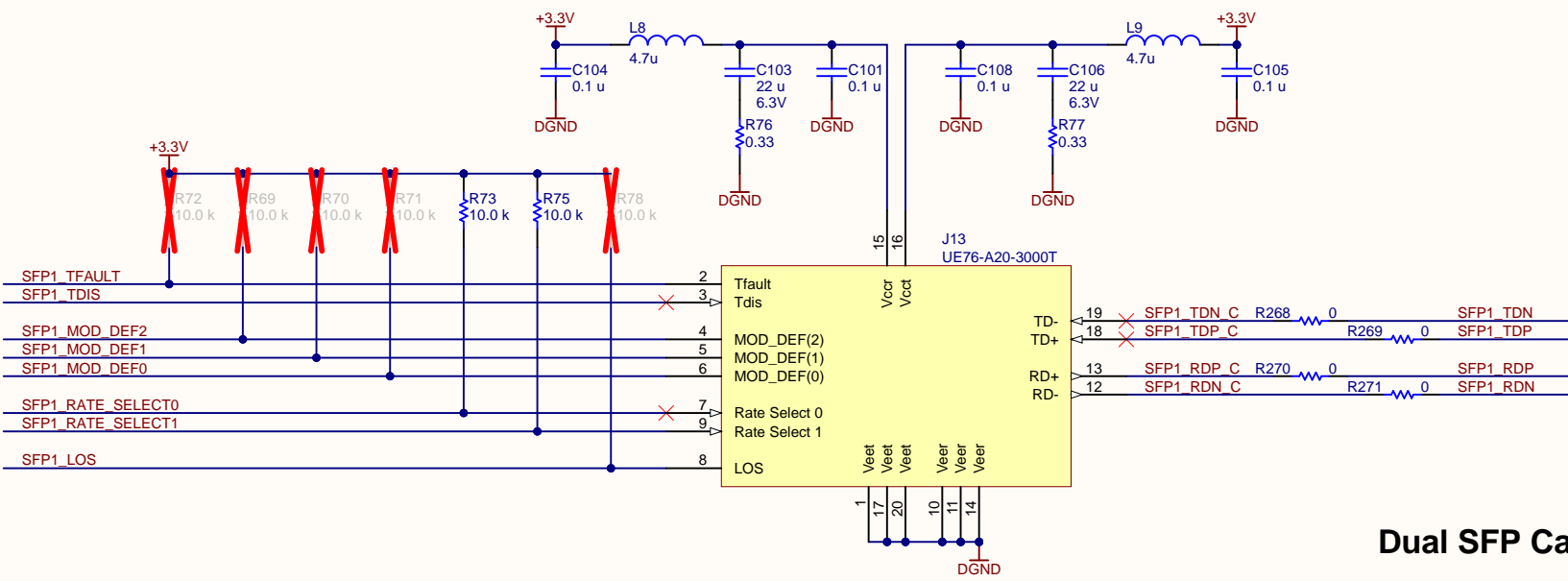
DDR Reference Clock



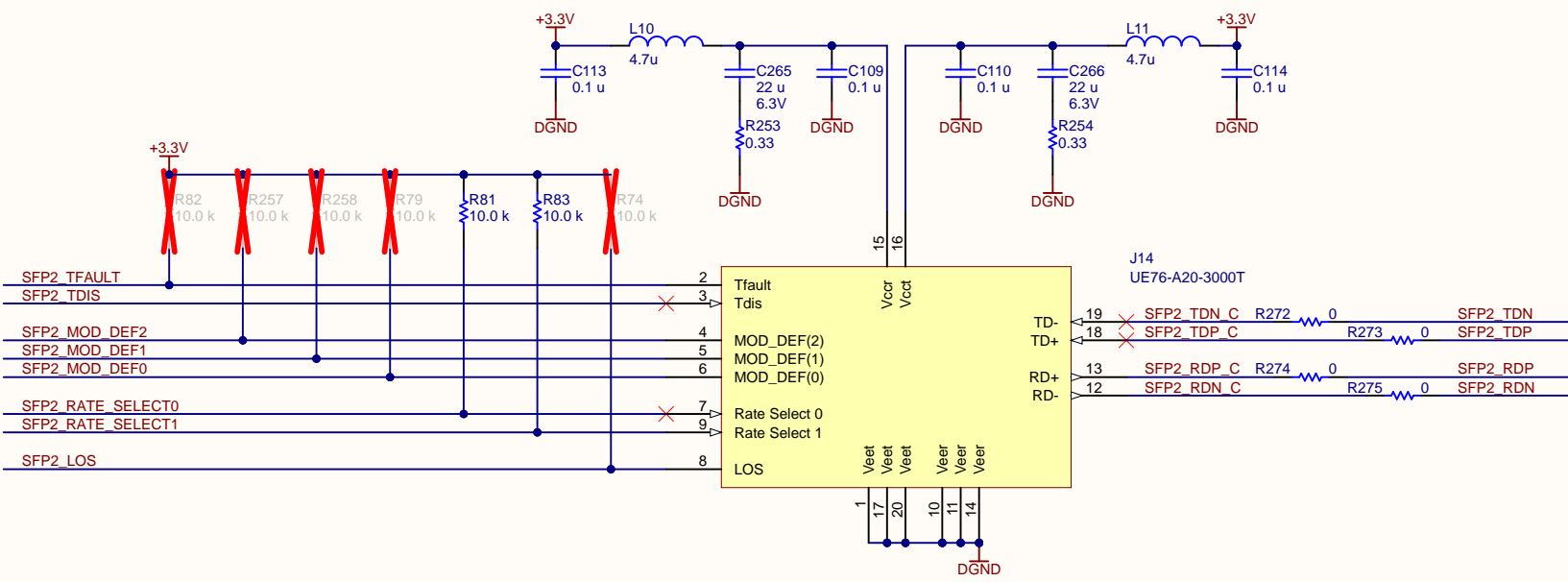
SFP Level Translators



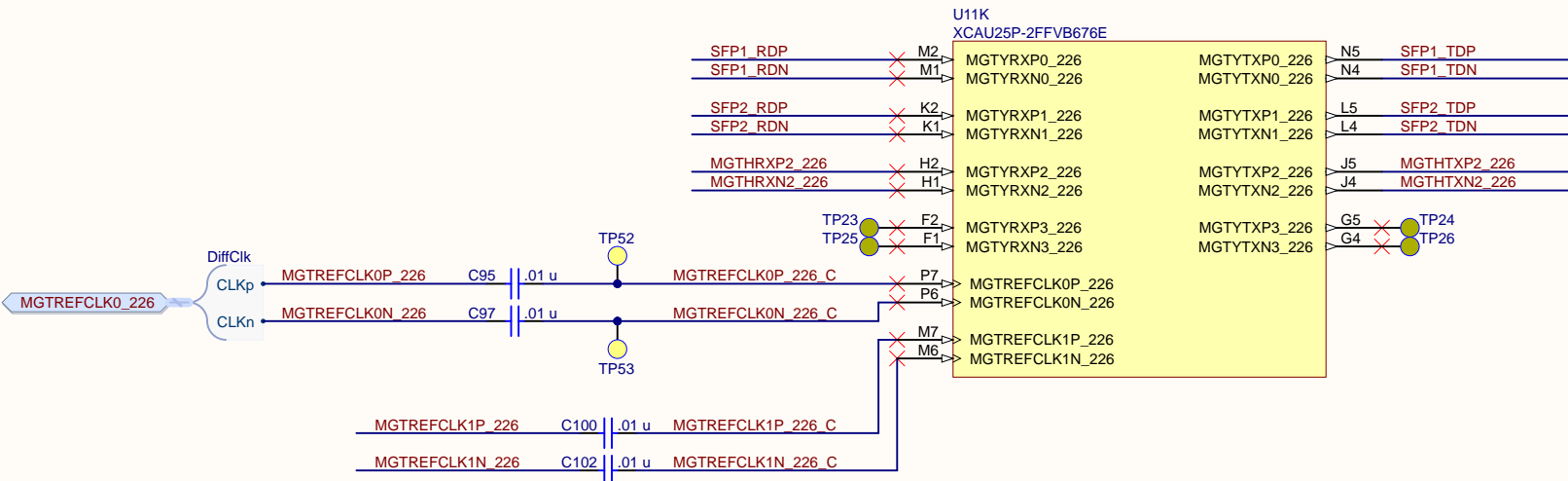
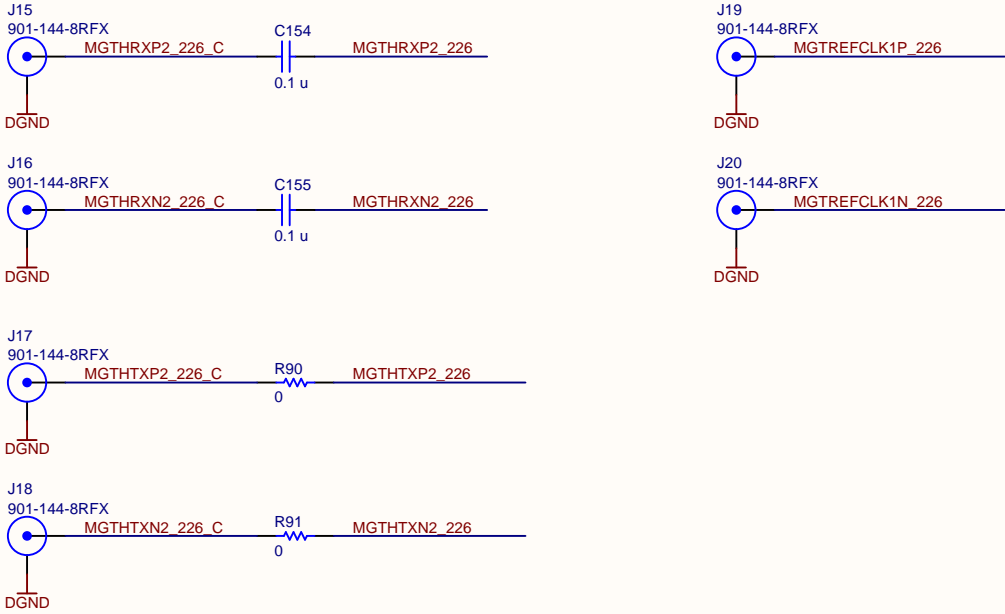
SFP+ Transceiver 1



SFP+ Transceiver 2

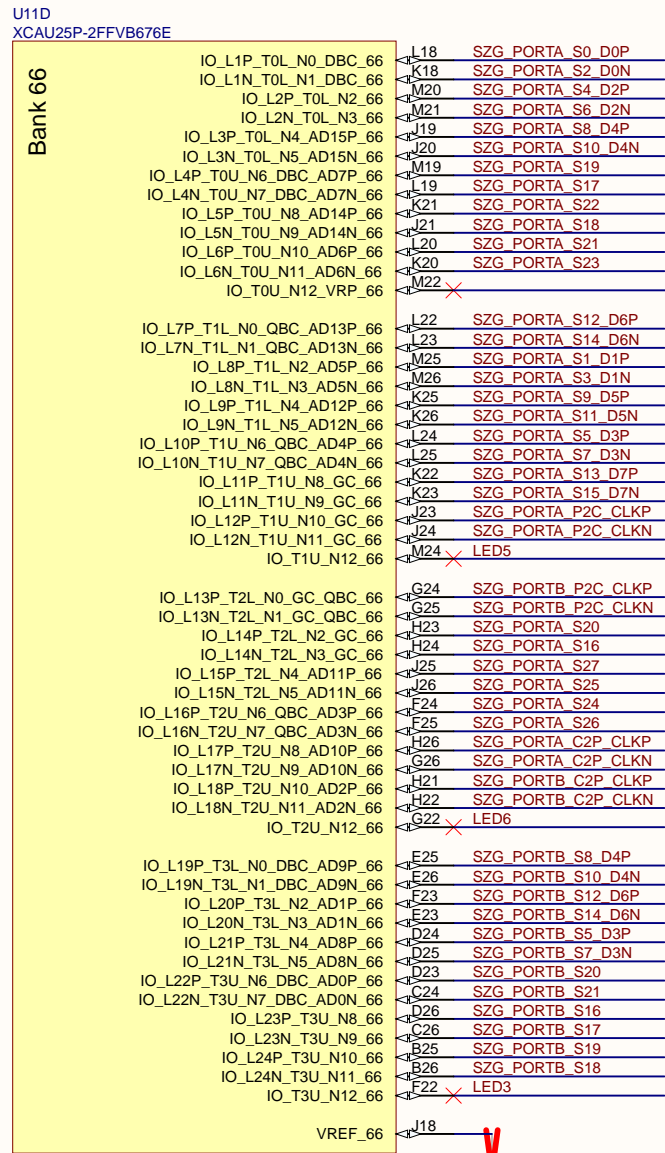


SMA Connectors

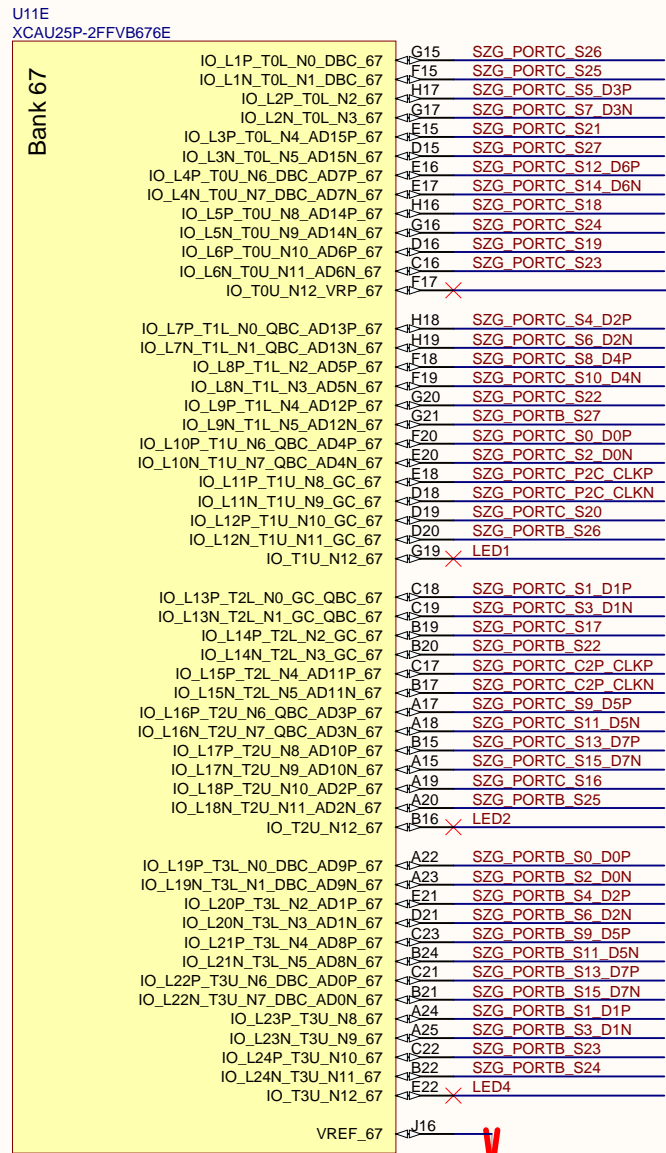


AUS+ Bank 226

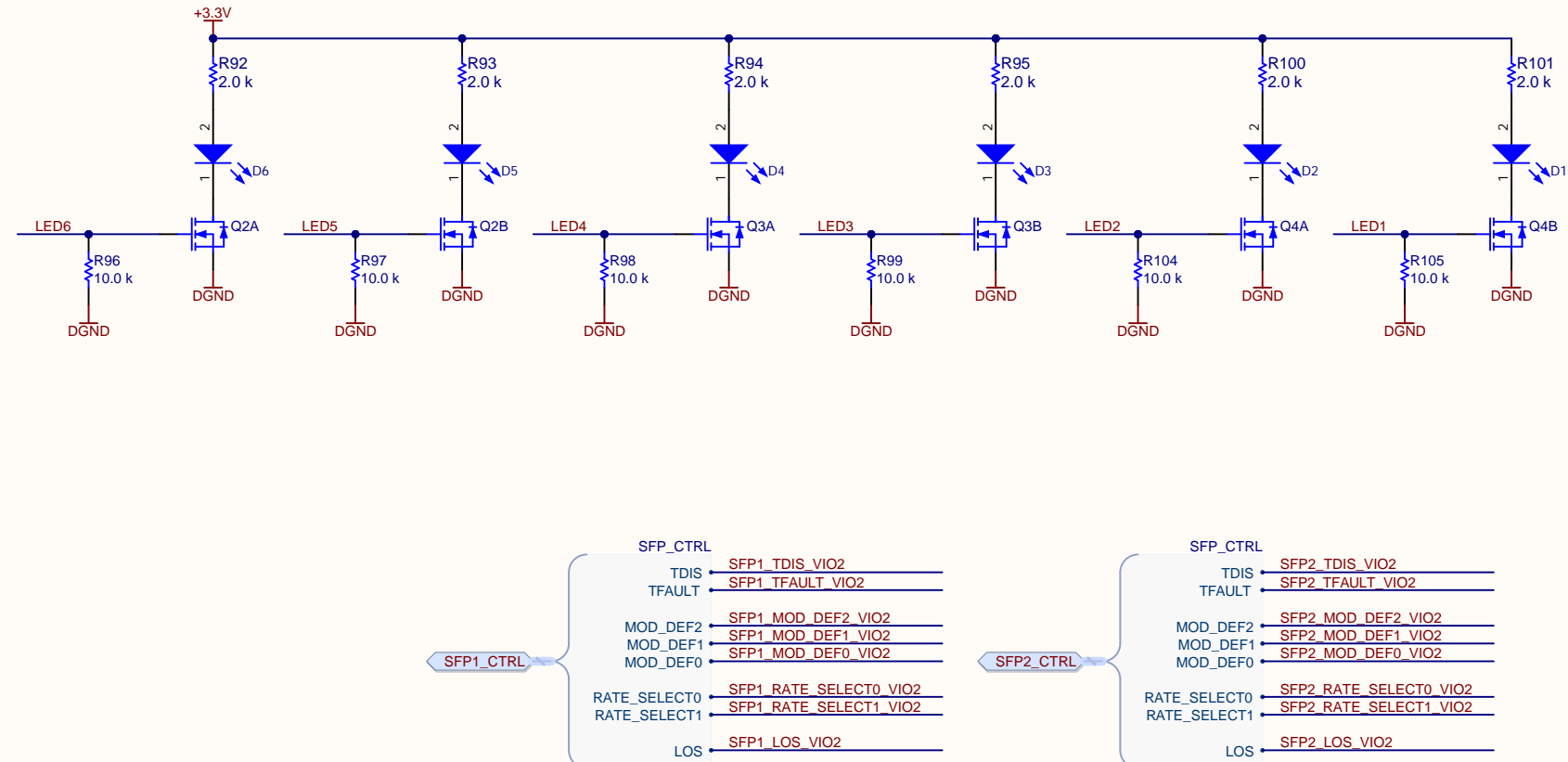
AUS+ B66



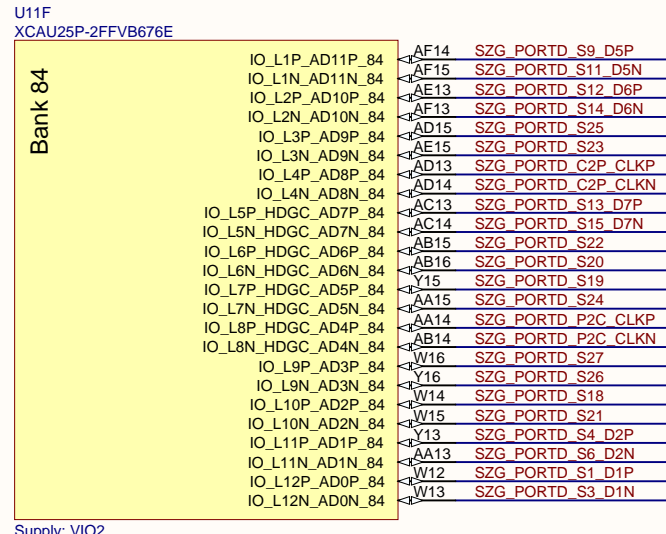
AUS+ Bank 67



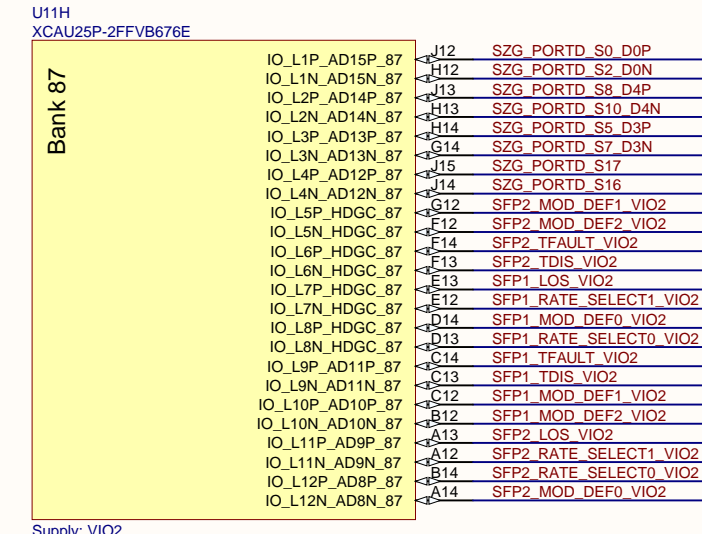
User LEDs



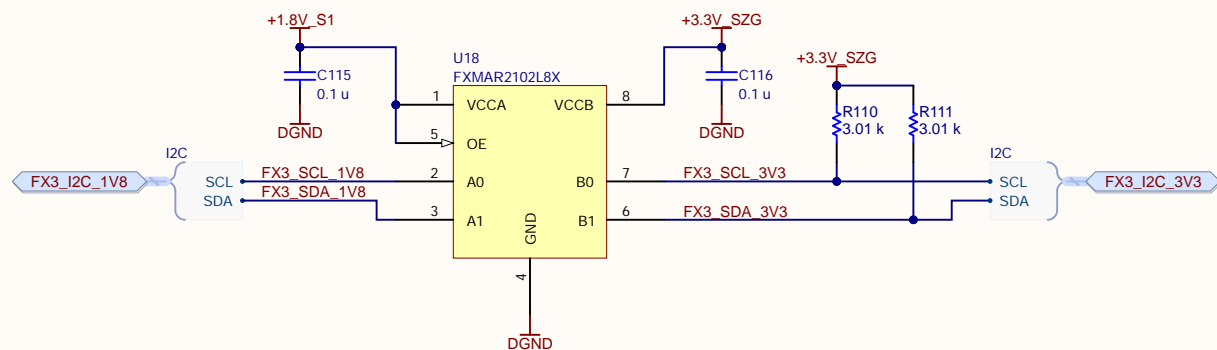
AUS+ Bank 84



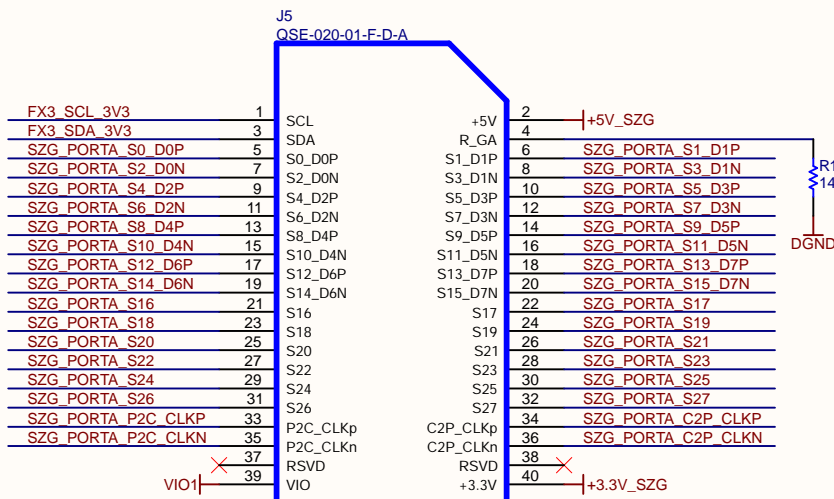
AUS+ B87



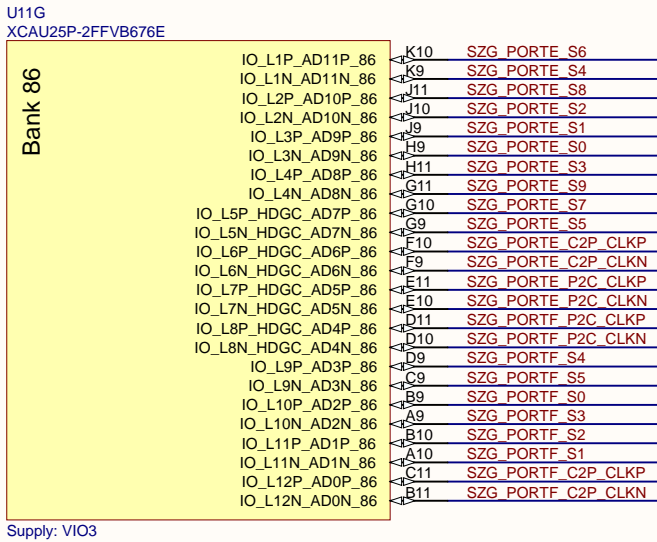
I2C Level Shifter



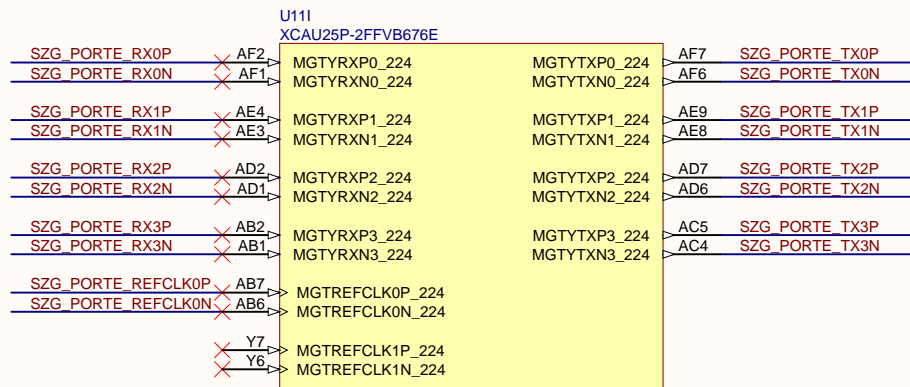
SYZYGY PORT A (VIO1)



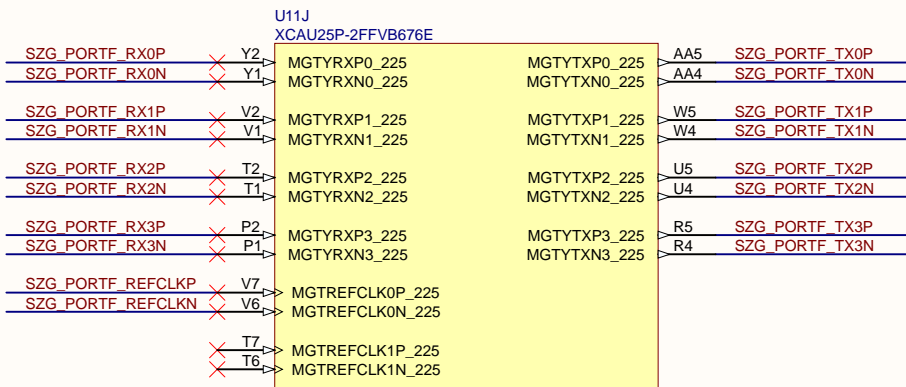
AUS+ Bank 86



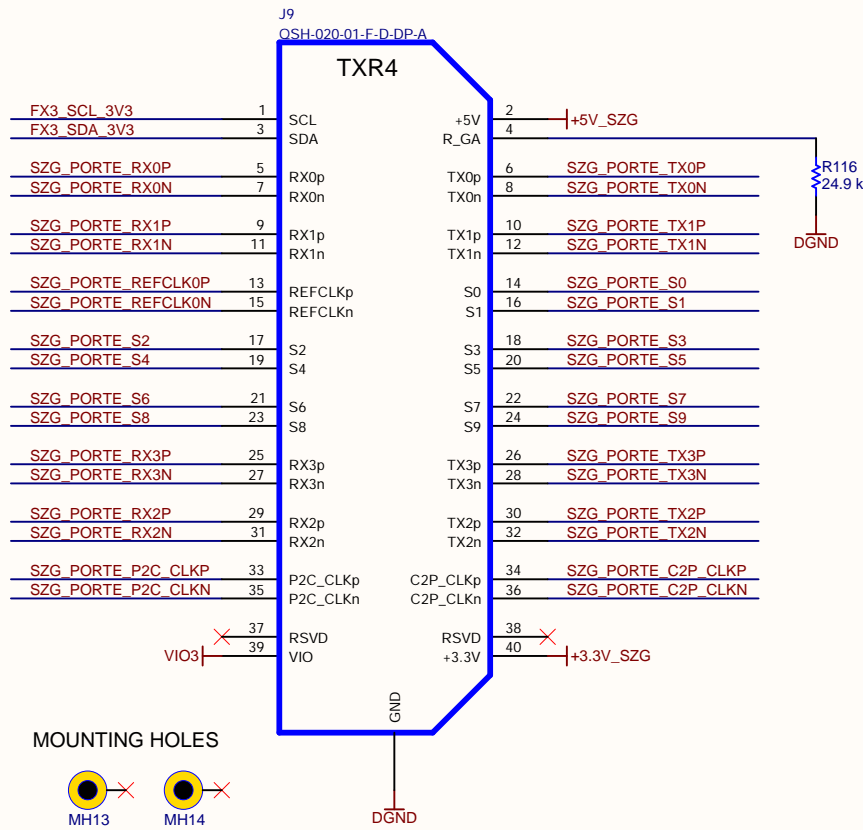
AUS+ Bank 224



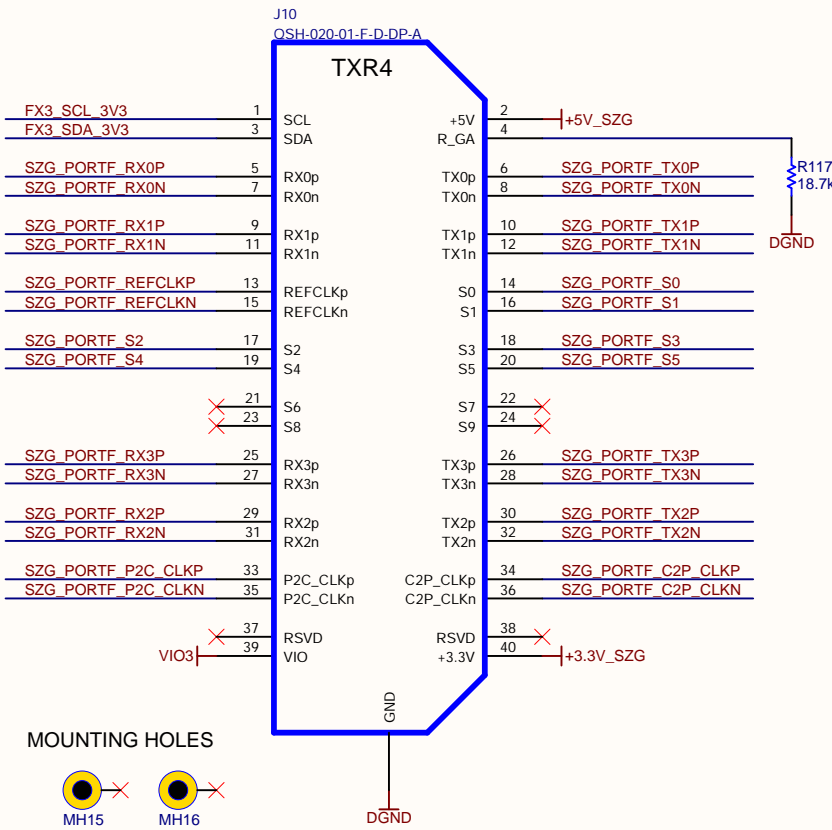
AUS+ Bank 225

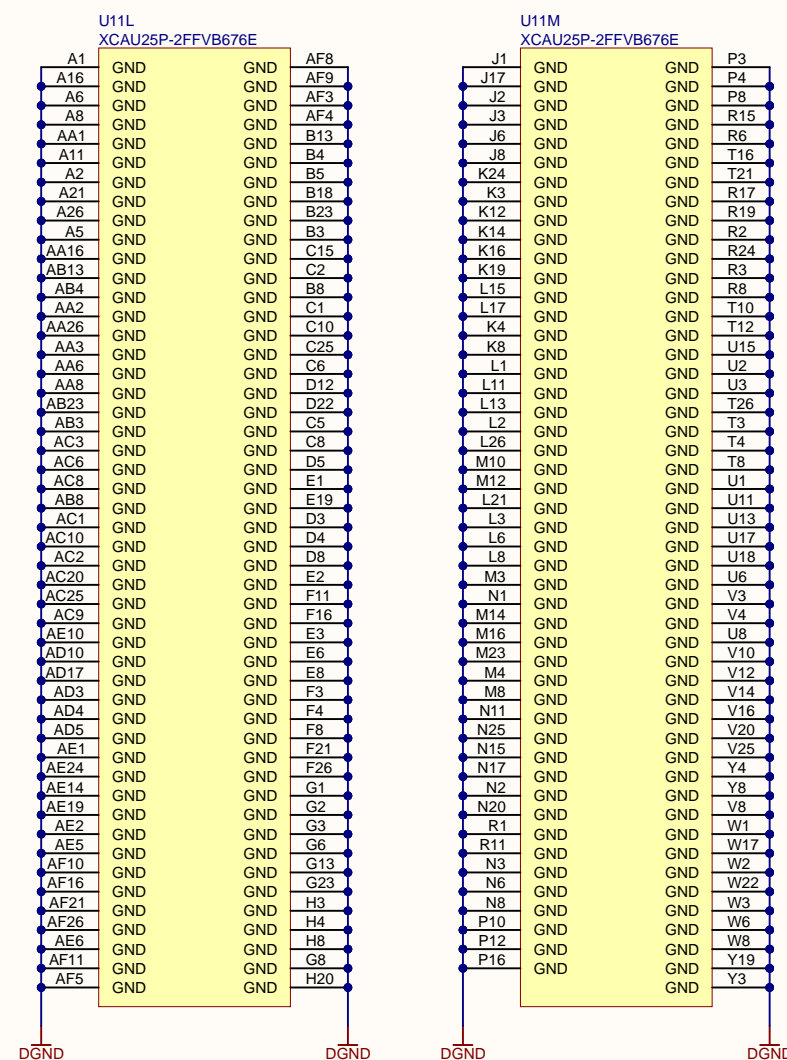
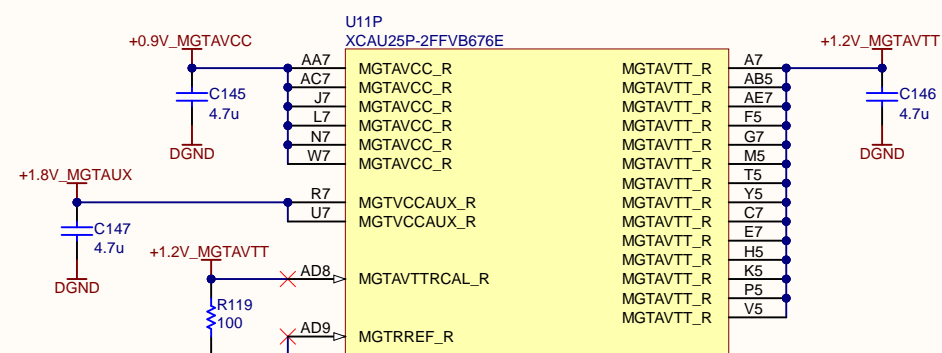
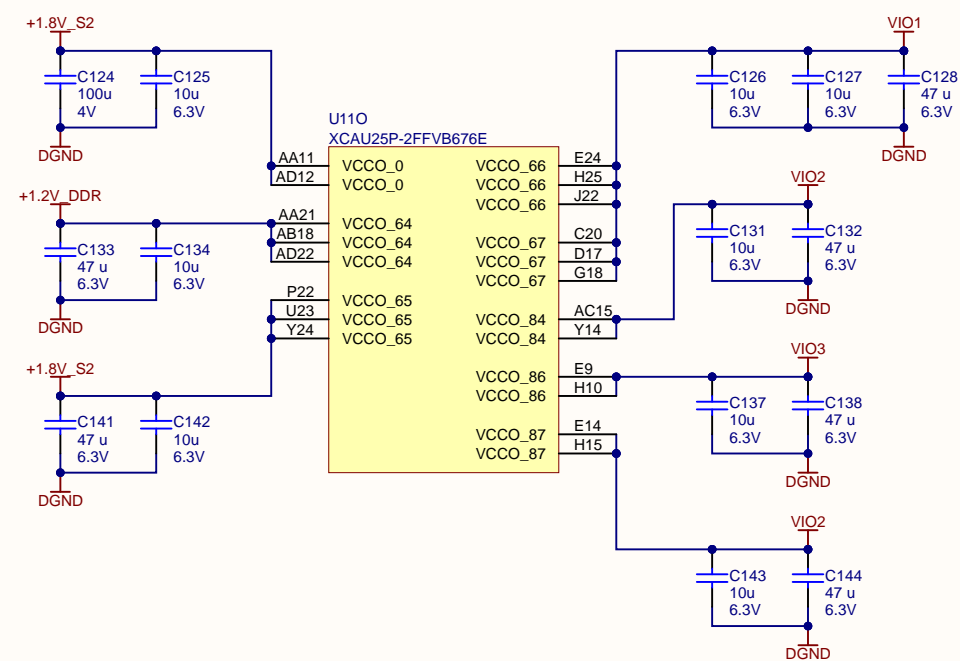
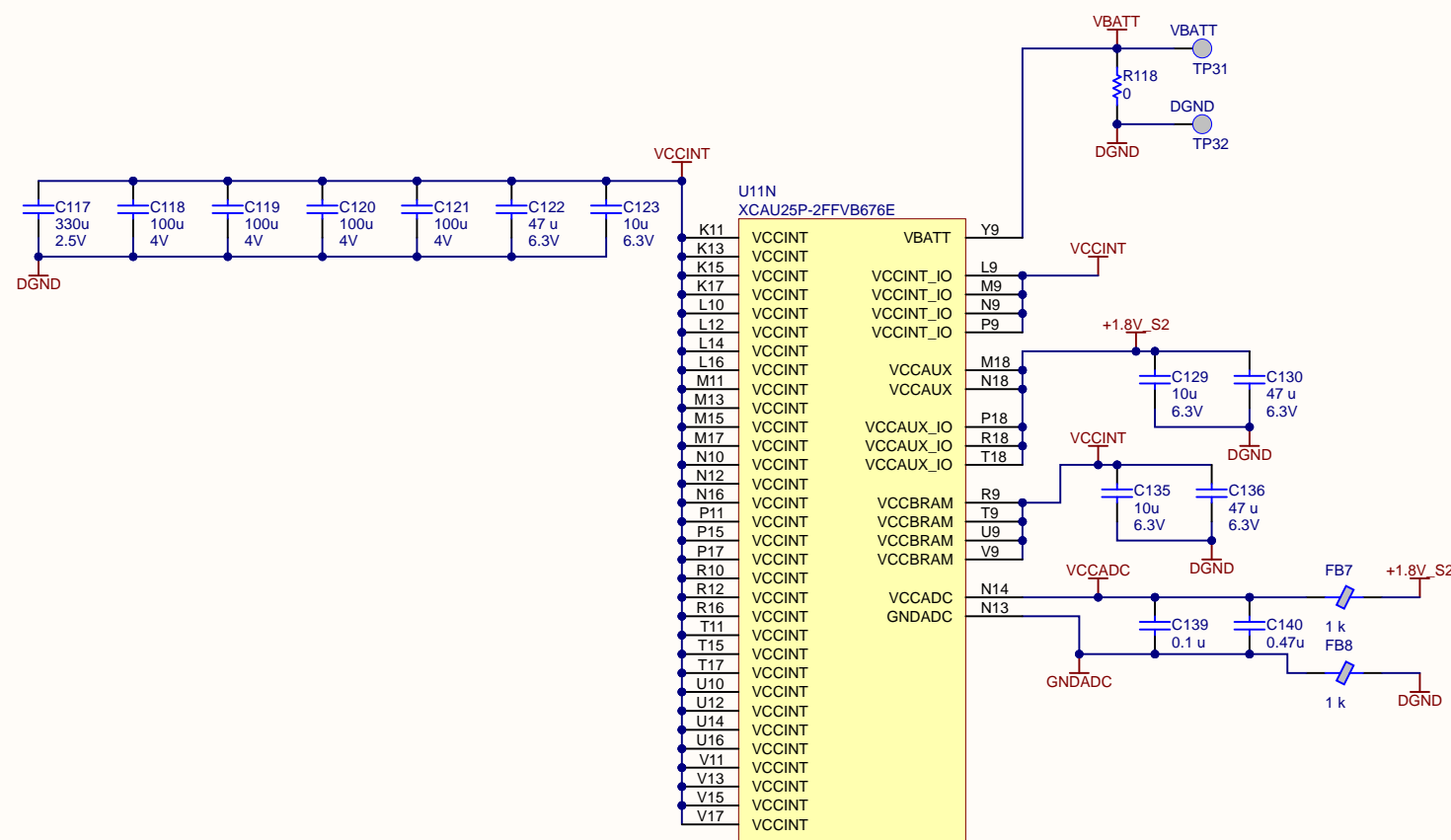


SYZYGY PORT E (VIO3)

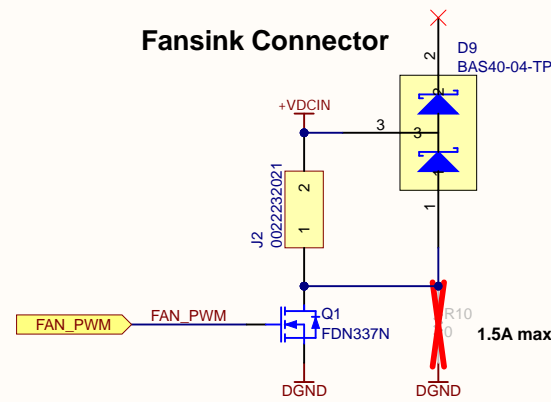


SYZYGY PORT F (VIO3)



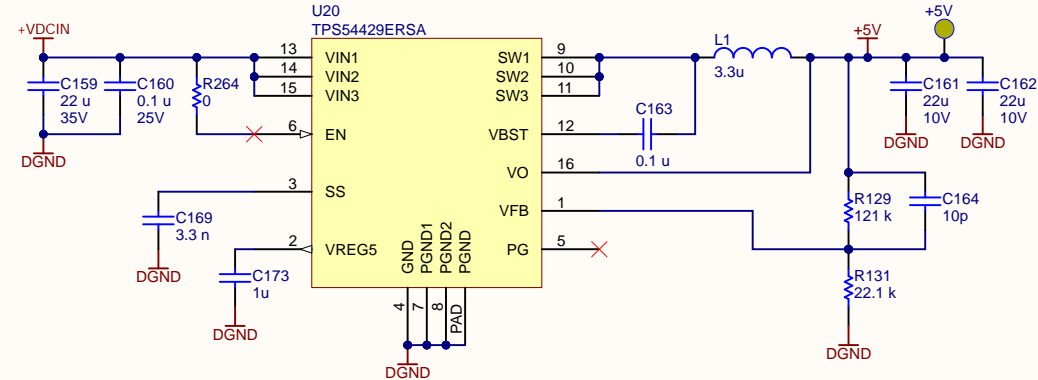


Fansink Connector



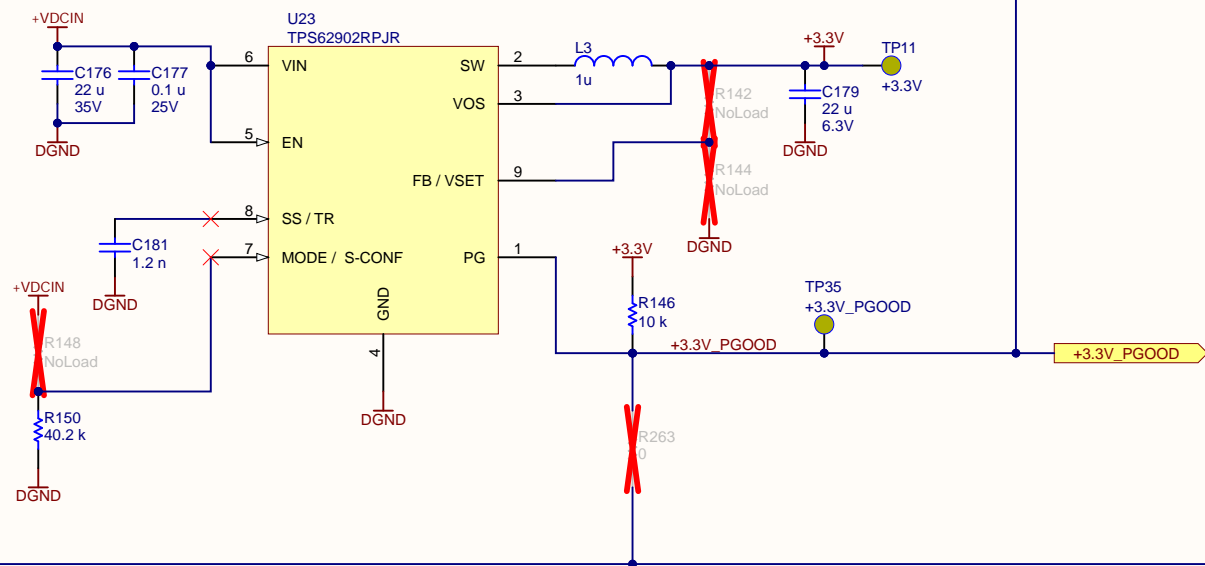
+5V Step-Down

4.5 A



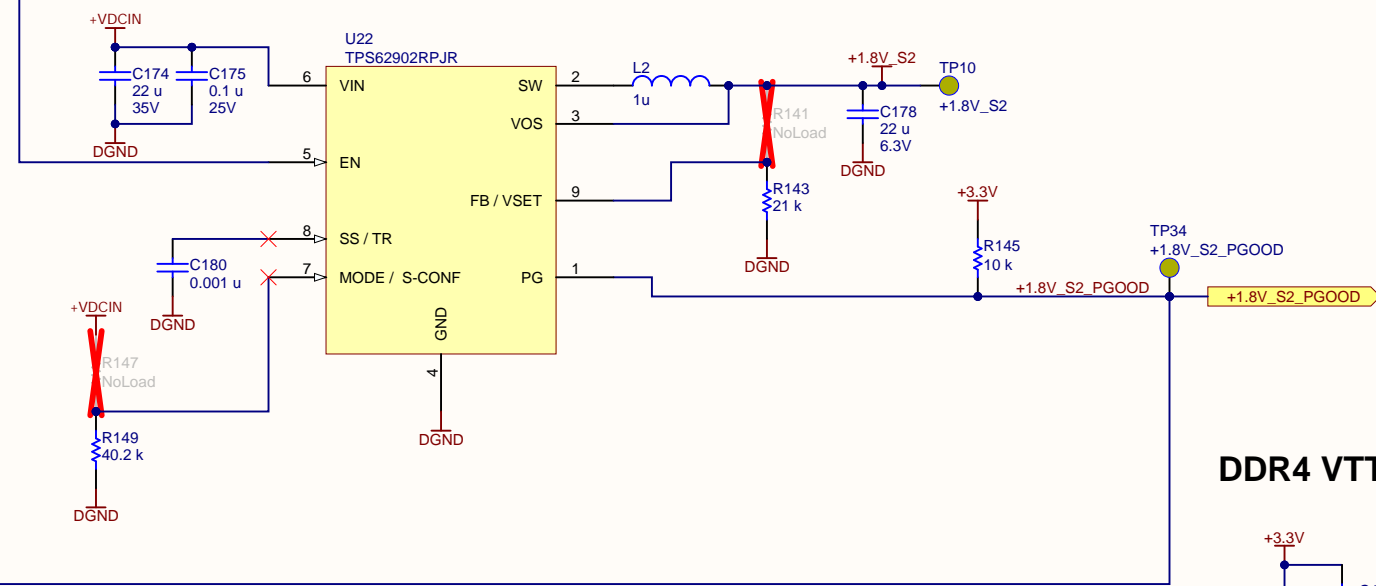
+3.3V Step-Down

2 A



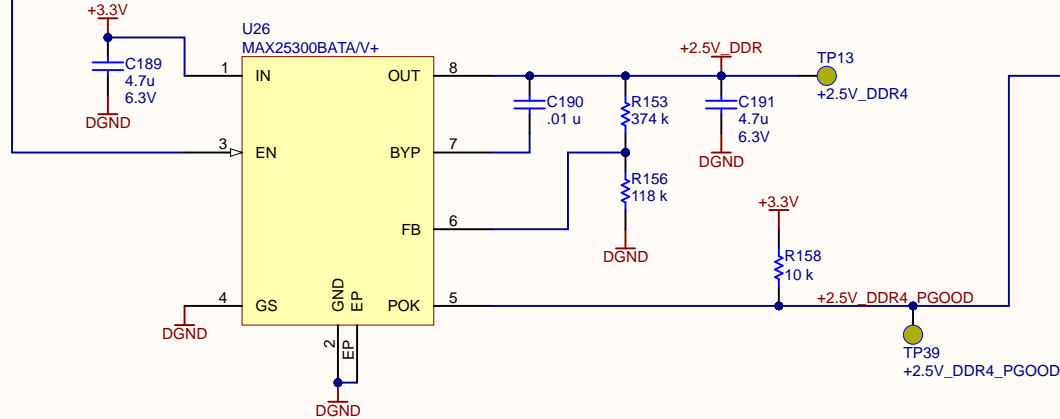
+1.8V Step-Down

2 A



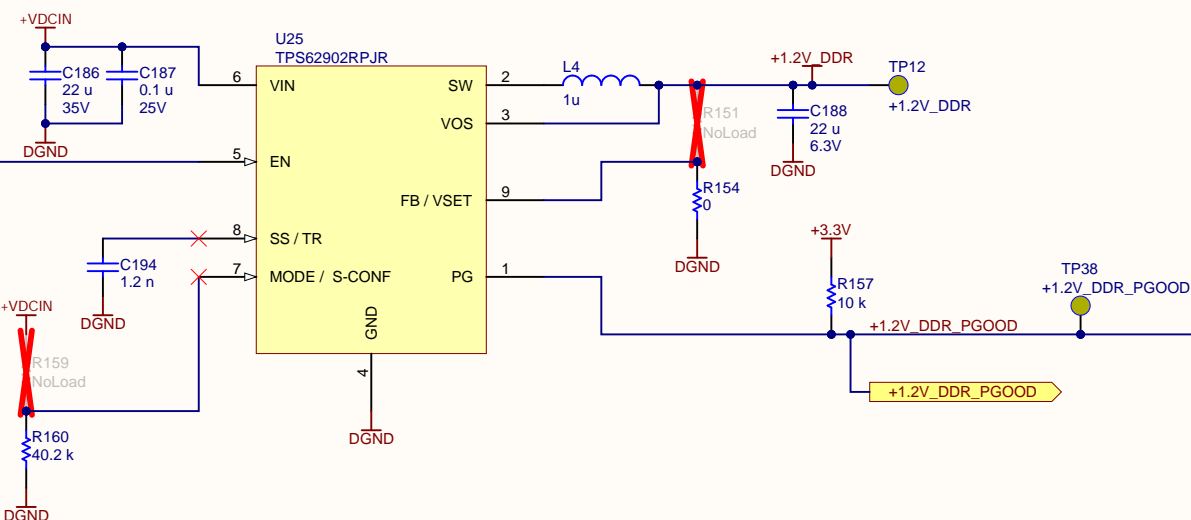
DDR4 VPP

500 mA @ 2.5 V

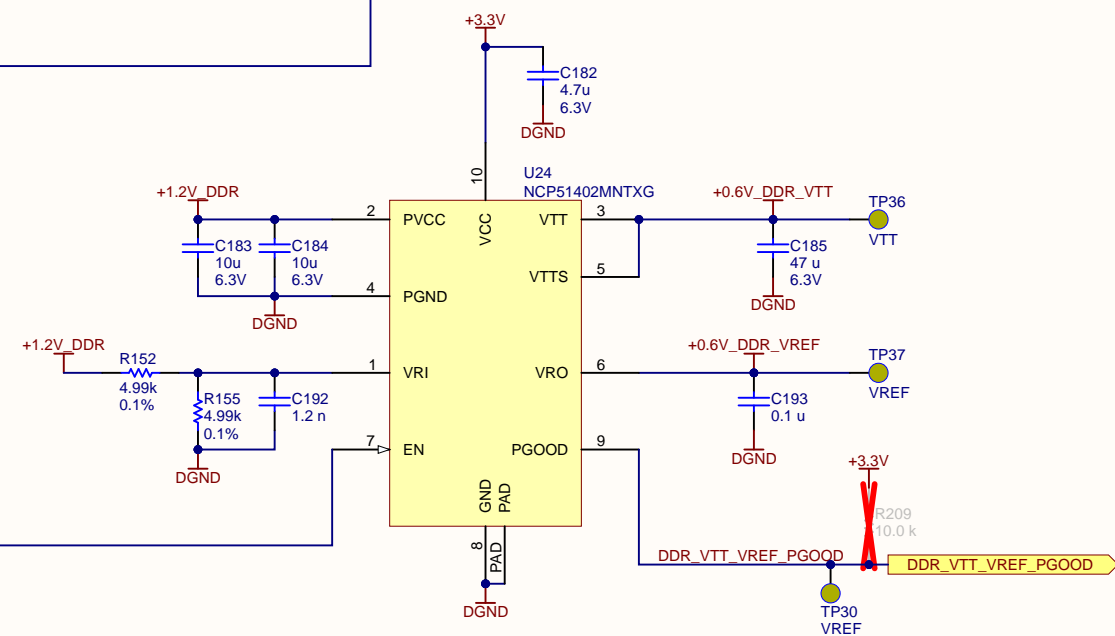


DDR4 Core

2 A @ 1.2 V



DDR4 VTT / VREF



Barrel Jack

5A max
2.5/5.1 mm

Reverse/Overvoltage Protection

8-14 VDC

6-pin Mini-Fit

24A max

Power Switch

I2C I/O Expander

EN5311 Output Voltage Settings				
VS2	VS1	VS0	Vout	
0	0	0	3.3V	
0	0	1	2.5V	
0	1	0	1.8V	
0	1	1	1.5V	
1	0	0	1.25V	
1	0	1	1.2V	
1	1	0	0.8V	
1	1	1	User	

VIO1 Step-Down

1 A

VIO2 Step-Down

1 A

VIO3 Step-Down

1 A

+1.8V_AUX Step-Down

4.5 A

MGTAVCC LDO

3 A @ +0.9V

MGTAVTT LDO

3 A @ +1.2V

MGTVCCAUX LDO

500 mA @ +1.8V

POWER-GOOD LED

+5V Step-Down - SYZGY

4.5 A

+3.3V Step-Down - SYZGY

4.5 A

INPUT POWER LED